



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Confirmation No.: 4758

Nobutaka TANIGUCHI

Art Unit: 2816

Serial No.: 09/777,897

Examiner: L. Nguyen

Filed: February 7, 2001

Docket No.: 100353-00039

For: DELAY TIME ADJUSTING METHOD OF DELAYING A PHASE OF
AN OUTPUT SIGNAL UNTIL A PHASE DIFFERENCE BETWEEN
AN INPUT SIGNAL AND THE OUTPUT SIGNAL BECOMES AN
INTEGRAL NUMBER OF PERIODS OTHER THAN ZERO

STATEMENT OF COMMON OWNERSHIP

Commissioner for Patents
Washington, D.C. 20231

August 23, 2004

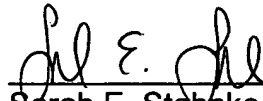
Sir:

United States Patent Number 6,266,294 to Yada et al., issued July 24, 2001, filed May 4, 1999 was assigned to Fujitsu Limited, or was subject to an obligation of assignment to Fujitsu Limited, at the time the invention of the present application was made. The present application was also assigned to Fujitsu Limited, or was subject to an obligation of assignment to Fujitsu Limited, at the time the invention of the present application was made. Accordingly, the present application and Yada were commonly owned by, or were subject to an obligation of Assignment to, the same person under 37 C.F.R. § 1.104(a)(5)(i) at the time the invention of the present application was made.

The Commissioner is authorized to charge payment for any additional fees which may be required with respect to this paper to Deposit Account No.

01-2300, referencing Attorney Docket No. 100353-00039.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "S. E. Stahnke", is written over a horizontal line.

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